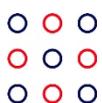


Boot Camp-Special Ops Challenge Quiz

1. What's the critical difference between a panel and dialog window?
 - a. There is none
 - b. Panels must be closed to continue editing, whereas dialogs can be left open
 - c. Dialogs must be closed to continue editing, whereas panels can be left open
2. What is the recommended grid size for the schematic symbols?
 - a. 10 Millimeters
 - b. 10 Mils
 - c. 10 Inches
 - d. 100 Mils
3. Which of the following provides settings for ALL projects opened within the tool?
 - a. Tools>>Preferences
 - b. .PrjPCB file
 - c. Project >> Options menu command
 - d. Tools >> Document Options (This now only exists in the Schematic Library editor; otherwise it's covered in the Properties panel. Not sure how to word that.)
4. The query language is only used for design rules in the PCB editor.
 - a. True
 - b. False
5. The only electrical primitive provided in the schematic symbol library editor that is not provided in the schematic editor is the:
 - a. Component
 - b. Line
 - c. Pin
 - d. Pad
6. In the schematic editor, pins can be moved or deleted:
 - a. True
 - b. False



Boot Camp-Special Ops Challenge Quiz

7. An Integrated Library is required for a project.
 - a. True
 - b. False

8. A project must have a dedicated library.
 - a. True
 - b. False

9. One can create a schematic and PCB library from a project's schematics and PCB layout, respectively.
 - a. True
 - b. False

10. The user can add a property to a primitive.
 - a. True
 - b. False

11. A component parameter consists of:
 - a. A name and a numerical value
 - b. 2 numerical values
 - c. Name and Value

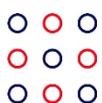
12. The place menu items of the PCB editor are the same as the schematic editor.
 - a. True
 - b. False

13. When a filter is applied in any of the editors, which of the following can be used to remove the filter every time?
 - a. Simply clicking on the editor window
 - b. Using the Shift+C shortcut
 - c. Hitting the Escape key



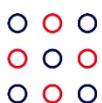
Boot Camp-Special Ops Challenge Quiz

14. If one links a 3D model directly to the schematic symbol, it can be viewed in the PCB editor by pressing the 3 key.
- a. True
 - b. False
15. One can create a polygon pour in the PCB library editor.
- a. True
 - b. False
16. What workspace panel is used to place components onto the schematics?
- a. Projects Panel
 - b. Navigator Panel
 - c. Components Panel
 - d. Storage Manager Panel
17. The electrical grid is ...
- a. The same as the snap grid
 - b. The distance before the cursor snaps to a wire or pin hot spot
 - c. The grid that is seen when one zooms in
18. Which of the following statements about the schematic template is true?
- a. The schematic template must use the features in Tools >> Document Options
 - b. The schematic template is saved in Project >> Project Options
 - c. The schematic template can be graphically edited at any time in the project schematic
 - d. The schematic template has the extension .schdot
19. One can have different designators in the schematic and PCB for the same component.
- a. True
 - b. False



Boot Camp-Special Ops Challenge Quiz

20. The workspace panel used in the schematic, PCB, schematic library, and PCB library to perform global editing is/are:
- a. Properties Panel
 - b. Filter Panel
 - c. PCB Panel
 - d. List Panel
 - e. Properties Panel and List Panel
21. The workspace panel used in the schematic, PCB, schematic library and PCB library to globally select components is/are:
- a. Properties Panel
 - b. Filter Panel
 - c. List Panel
 - d. Filter Panel and List Panel
22. Which workspace panel allows for smart grid paste and smart grid insert?
- a. Properties Panel
 - b. Filter Panel
 - c. List Panel
23. In the PCB editor, which default hotkey is used to open the View Configuration panel?
- a. L
 - b. M
 - c. C
 - d. V
24. To add a plane or signal layer, which tool is used in the PCB editor?
- a. View Configuration
 - b. Layer Stack Manager
 - c. PCB Panel



Boot Camp-Special Ops Challenge Quiz

25. What file type can be generated and provided to the fabricator?
- a. Gerber
 - b. Gerber 2X
 - c. ODB++
 - d. IPC-2581
 - e. Any of the above
26. A union of devices can only be created in the PCB Editor.
- a. True
 - b. False
27. If a mechanical layer is used in a footprint, that mechanical layer will be enabled in the PCB editor if that footprint has been placed.
- a. True
 - b. False
28. If a design rule is syntactically incorrect, what should you NOT do?
- a. Attempt to fix it
 - b. Disable it
 - c. Fix it when you can get back to it
29. Suppose there are several rules established for the electrical clearance, each with different scopes. If a net name shows up in two or more scopes for a particular rule, Altium Designer will
- a. Test each rule listed against the net
 - b. Only test the net with the highest priority rule
 - c. Only test the net with the rule containing the scope and has the highest priority of all other rules that may have the same scope.
30. Suppose there are several rules established for the electrical clearance, each with different scopes. If a scope that covers all primitives (ALL-ALL rule) is given priority 1, all the other rules will be ignored.
- a. True
 - b. False

